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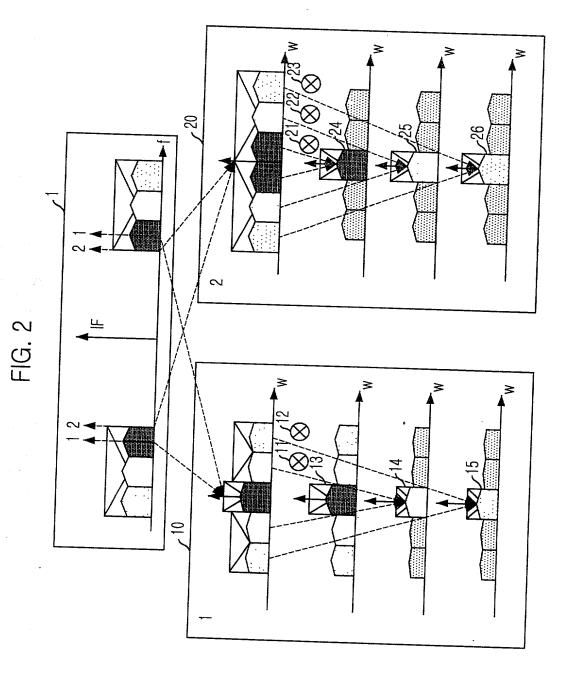
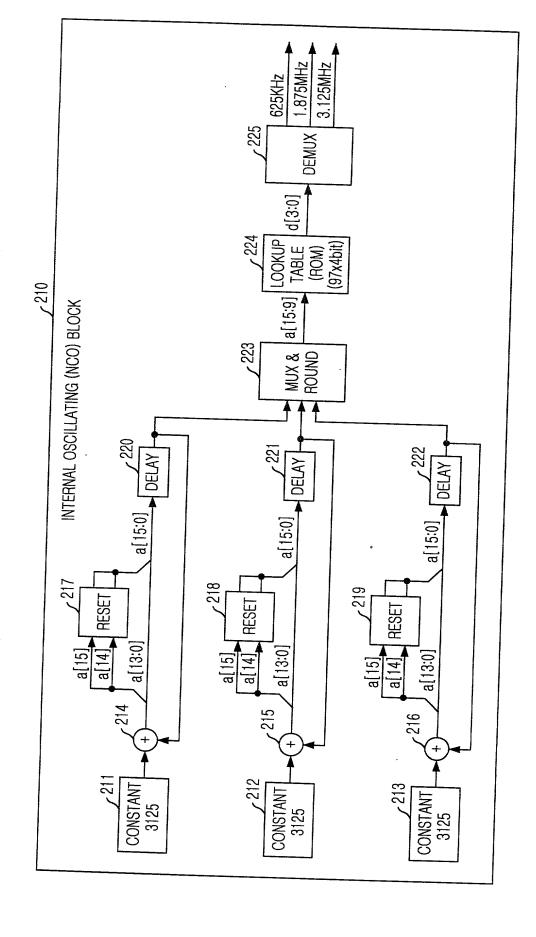
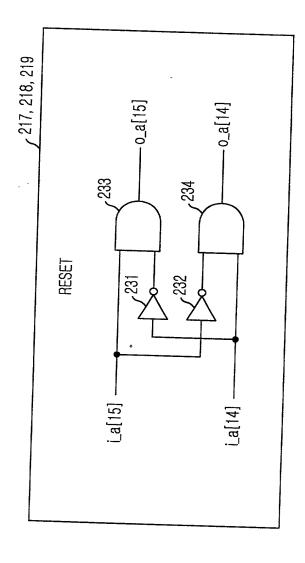


FIG. 3



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FIG. 4



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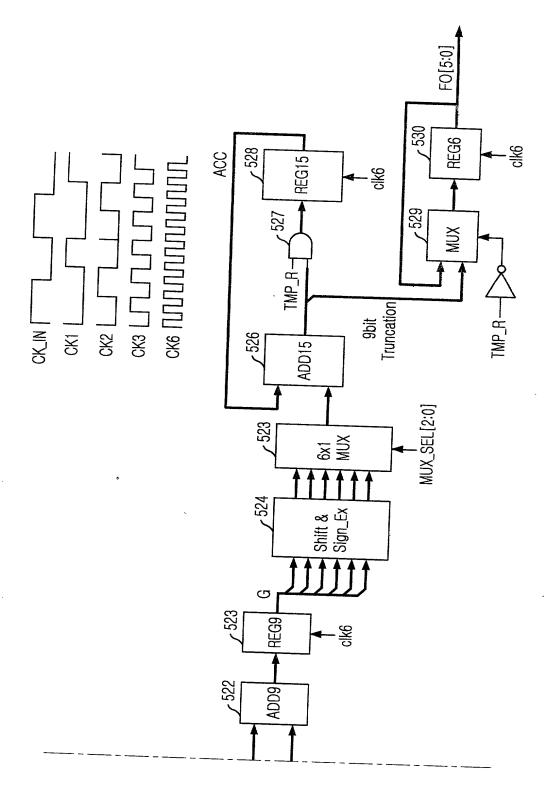
521 REG9 REG9 <u>S</u> 519 520 ADD9 ADD9 518 REG9 REG9 REG9 REG9 <u>S</u> 516 517 515 ALU9 ALU9 ALU9 ALU9 513 REG9 REG9 REG9 REG9 REG9 REG9 REG9 왕 .512 505 .506 508 509 .510 2'LUT\_0 507 2<sup>7</sup>LUT\_2 2'LUT\_3 2'UJ\_4 2'LUT\_5 2'LUT\_6 2'LUT\_1 5 2'LUT\_7 8 504 XOR XOR XOR XOR XOR XOR X 0 8 XOR A[22:16] A[46:40] A[30:24] A[54:48] A[38:32] A[14:8] A[55] A[47] A[39] A[15] [23:16] [39:56] A[63:56] A[55:48] A[47:40] A[31:24] A[15:8] A[7:0] 503 <u>%</u> % 9.8304 MHz REG64 REG64 REG64 REG64 REG64 502 REG64 50 FI[4] F[3] FI[2] H[5] FI[5:0]

FIG. 5A

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FIG. 6

